

Elements of Digital Logic Student Guide Version 1.0
2003 Printing
Errata as of 10/10/03

- Page 8: Parallax phone number given is: (916) 624-833 . The correct number is: (916) 624-8333.
- Page 75: Throughout the section discussing the "Clocked RS Latch", the gates are referred to as **NAND Gates**, when in fact the gates used are **NOR Gates**. Figure 6-4 is correct.
- Page 76: Activity #2's title should be **Building the Clocked RS-Latch**. Just as on page 75, the gates are referred to as **NAND Gates** when they should be called **NOR Gates**. Figure 6-5 is correct.
- Page 81: There is an extra "t" in the last sentence on page 81. And, that's all I have to say about that.
- Page 89: Figure 7-4 shows an "extra" wire that should not be there. Specifically, NOR A should have only one input: PB1. The wire connecting NOR A to oblivion should not be there.
- Page 97: Exercise 2 should read as: "Describe what happens when either the set or the reset pushbuttons are held and the input pushbutton is held.
- Page 110: Period at the end of question #1 of "Extra for Experts" should be removed.

The above errata was corrected on 9/22/04 for the 2004 reprint of v1.0. The item below remains uncorrected for both the 2003 and 2004 printing.

- The actual PCB is correct, but the text show images with the wrong designators for X15, 16 throughout.